

041762517

7/4	739
Class	Subclass
ISSUE CLASSIFICATION	

FILED UNDER 35 U.S.C. 373

**PATENT NUMBER**

**U.S. UTILITY Patent Application**

**O.I.P.E.**

**PATENT DATE**

SCANNER

11

100

APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/762517	D F	714	739	2184 SS	Deady Deady

## APPLICANTS

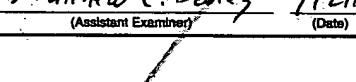
Franz Hubner

734

## Integrated circuit with an integrated module test

PTO-2040  
12/89

## **ISSUING CLASSIFICATION**

<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>			<b>CLAIMS ALLOWED</b>	
	Sheets Drwg.	Figs. Drwg.	Print. Fig.	Total Claims	Print. Claim for O.G.
	4	7	1	6	1
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	 Matthew C. Decady (Assistant Examiner) (Date)			<b>NOTICE OF ALLOWANCE MAILED</b>   5/4/04	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____  _____  _____	 ALBERT DECADY SUPERVISORY PATENT EXAMINER TECHNOLOGY CENTER 2100 (Primary Examiner)			<b>ISSUE FEE</b>  Amount Due      Date Paid  \$1,330.00	
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.				<b>ISSUE BATCH NUMBER</b>	

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